19 August 2025

To Whom It May Concern:

On behalf of the CHERI Alliance, we are pleased to offer our strong support for **Wyvern Global**, one of our member companies, in its efforts to design and deliver silicon solutions integrating CHERI technology.

The CHERI (Capability Hardware Enhanced RISC Instructions) architecture represents a transformative advance in computer security and reliability, preventing the root cause of ~ 70% of current cyberattacks. While research prototypes and software emulation have already demonstrated CHERI's technical merits, widespread adoption requires robust and commercially viable silicon implementations.

Wyvern Global's initiative to create chips embedding CHERI technology is therefore both timely and essential. Hardware availability is the critical next step to allow software ecosystems, developers, and industry partners to fully exploit the benefits of CHERI. By providing production-grade silicon, Wyvern Global will accelerate the transition from research to real-world deployment, catalysing adoption across industries where security, safety, and resilience are paramount.

We also recognise the significant market potential for such products. As cyber threats continue to evolve, enterprises, governments, and technology providers are urgently seeking hardware-assisted solutions that can deliver strong security guarantees without compromising performance. Chips that integrate CHERI offer a compelling competitive edge, with opportunities spanning embedded systems, critical infrastructure, industrial applications and many other devices.

The CHERI Alliance is committed to supporting CHERI adoption, and we look forward to collaborating with Wyvern Global as they advance the state of the art. We are confident that their efforts will make a substantial contribution to the CHERI ecosystem and to the broader mission of building a safer and more trustworthy digital infrastructure.

Sincerely,

Mike EFTIMAKIS
Founding Director
CHERI Alliance

Josh Tranchant, CEO, Wyvern Global Ltd
53 Broad Lane,
Lichfield,
Staffordshire,
WS14 9SU
admin@wyvern.global
3rd September 2025

BT Group PLC's support for CHERI Technology and the surrounding ecosystem

Dear Josh,

On behalf of BT Group, I am writing to express our strong support and encouragement for systems integrators, manufacturers and other related vendors to engage with the CHERI ecosystem. We believe that addressing memory safety is a critical development area for the cybersecurity resiliency of telecommunications and critical national infrastructure.

BT Group is the UK's leading provider of fixed and mobile telecommunications and related secure digital products, solutions and services. We also provide managed telecommunications, security and network and IT infrastructure services to customers across 180 countries. BT Group consists of three customer-facing units: Consumer serves individuals and families in the UK; Business covers companies and public services in the UK and internationally; Openreach is an independently governed, wholly owned subsidiary wholesaling fixed access infrastructure services to its customers - over 650 communications providers across the UK.

BT Research and Network Strategy, in conjunction with BT Security, are actively investigating how adoption of memory-safe practices and hardware can bolster our industry. We recently joined the CHERI Alliance to embed ourselves within the community, and to display public support to systems integrators and chip designers that the pull from the telecommunications industry exists and it is sincere.

We foresee multiple use cases of CHERI for the telecoms industry which we wish to research and develop further. These include secured and resilient routing, out-of-band management controllers, and software development lifecycle gating processes. BT is also open and excited to work with partners in our supply chain to see CHERI more broadly adopted. CHERI is firmly

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placed in the technology roadmaps of both BT Security and BT Research and Network Strategy, in commitment to this goal.

Since joining the CHERI Alliance, we have forged a relationship with a RISC-V CPU design group, who we are collaborating with to produce an industrial demonstrator. This will be in the form of a network router, passing telecommunications workloads, with a CHERI processor hosting the operating system which oversees the control plane of the device. This demonstrator will be used to further signal to the industry, through publications, conference presentations and papers, that the industry can stand to benefit from CHERI hardware.

Yours sincerely,

Sam Cater
Research Technical Manager for Secure and Novel Compute

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